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Sheet 1 of 1

Complete if Known	
Application Number	09/608,624
Filing Date	June 30, 2000
First Named Inventor	Stephan J. JOURDAN et al.
Art Unit	2183
Examiner Name	Henry Tsai
Attorney Docket Number	02207/8609

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Document Number: Number - Kind Code[2] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
HT		US- 6,535,959	03-18-03	Ramprasad et al.	
		US- 6,427,188	07-30-02	Lyon et al.	
		US- 5,924,092	07-1999	Johnson	
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HT		US Publication No. 2002/0078327 A1	06-20-02	Jourdan et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document: Country Code[3] – Number[4] - Kind Code[5] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	English Abstract

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
HT		Bellas et al., "Architectural and Compiler Techniques for Energy Reduction in High Performance Microprocessors," *IEEE Transactions on VLSI*, Vol. 8, No. 3, June 2000, pp. 317-326.	
		Glaskowsky, Peter N., "Pentium 4 (Partially) Previewed," *Microprocessor Report*, Vol. 14, Archive 8, August 2000, p. 1, pp. 11-13.	
		Manne et al., "Pipeline Gating: Speculation Control for Energy Reduction," *ACM SIGARCH, Proceedings of the 25th Annual International Symposium on Computer Architecture*, IEEE Computer Society Tech. Comm. on Computer Architecture, June 27-July 1, 1998, Barcelona, Spain, pp. 132-141.	
		Papworth, David B., "Tuning the Pentium Pro Microarchitecture," *IEEE Micro*, IEEE Computer Society, Vol. 16, No. 2, April 1996, pp.8-15.	
		Solomon et al., "Micro-Operation Cache: A Power Aware Frontend for Variable Instruction Length ISA," *ISLPED '01*, August 6-7, 2001, Huntington Beach, CA, pp. 4-9.	
HT		Upton, Michael, "The Intel Pentium® 4 Processor," http://www.intel.com/pentium4, October 2000, 15 pp.	

Examiner Signature	/Henry Tsai/	Date Considered	12/23/04

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